

PALM INTRANET**Inventor Information for 10/020218**

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US 20050063480 A1	20050324	Method and System for OFDM Symbol Timing Synchronization	375/260		Sang, Tzu-Hsien et al.
US 20040021595 A1	20040205	Method and system for implementing a sigma delta analog-to-digital converter	341/144		Erdogan, Alper Tunga et al.
US 20030235245 A1	20031225	Method and system for computing pre-equalizer coefficients	375/232	375/233	Erdogan, Alper Tunga et al.
US 20030202612 A1	20031030	Method and system for rate enhanced SHDSL	375/265		Halder, Bijit et al.
US 20030118177 A1	20030626	Method and system for implementing a reduced complexity dual rate echo canceller	379/406.01		Karakas, Ahmet et al.
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US 20030112887 A1	20030619	Method and system for implementing weighted vector error echo cancellers	375/285	375/232	Sang, Tzu Hsien et al.
US 20030112861 A1	20030619	Method and system for adaptively training time domain	375/232	375/348; 375/350	Erdogan, Alper Tunga et al.

		equalizers			
US 20030112860 A1	20030619	Method and system for shortening channel impulse response using time domain equalization filter	375/229		Erdogan, Alper Tunga
US 20020136323 A1	20020926	Low-order HDSL2 transmit filter	375/296	375/232	Halder, Bijit
US 7076514 B2	20060711	Method and system for computing pre-equalizer coefficients	708/323		Erdogan; Alper Tunga et al.
US 6934328 B1	20050823	Relaxed, more optimum training for modems and the like	375/232	708/323	Halder; Bijit
US 6788236 B2	20040907	Method and system for implementing a sigma delta analog-to-digital converter	341/155	341/143	Erdogan; Alper Tunga et al.
US 6769090 B1	20040727	Unified technique for multi-rate trellis coding and decoding	714/792	714/795	Halder; Bijit
US 6693975 B2	20040217	Low-order HDSL2 transmit filter	375/297	708/426	Halder; Bijit
US 6642874 B1	20031104	Sigma-delta data conversion employing quantization error feedback	341/143		Lin; Chia-Liang et al.